

F16-2

newISS

NEW PROCESSOR

PRIOR ART

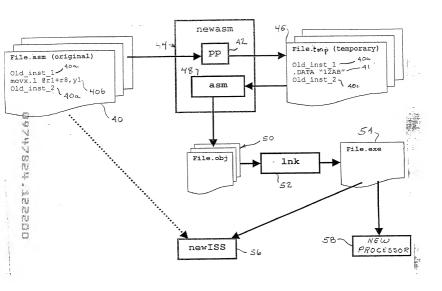


FIG-3

FIG. 4B